

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re: Appn. Ser. No.Div.Appn of : Art Unit Not Yet Ass'd
Appn. Ser. No.09/703,734 Filed. 11/1/00
Filed Herewith : Exr. Not Yet Ass'd
Inventors Cohen et al. : Atty Dkt No. YOR920000333US2

For: SEMICONDUCTOR RECESSED MASK INTERCONNECT TECHNOLOGY

INFORMATION DISCLOSURE TRANSMITTAL

Commissioner For Patents
P.O. Box 1450
Arlington, Va. 22313 - 1450

Sir:

Please find enclosed a listing (PTO Form 1449 type) of references with copies which may be relevant to the above identified application. This listing of references is not a representation that a search has been made, that better references than listed do not exist, or that other references are not applicable. Moreover this listing does not constitute any admission by applicants or applicants' attorney that the information provided herein is necessarily prior art to applicants invention.

Respectfully Submitted
Alvin J. Riddles 9/10/03
Alvin J. Riddles
Reg. No. 17862



LIST OF REFERENCES CITED BY APPLICANT
 (Use several sheets if necessary)

ATTY. DOCKET NO.
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APPLICANT
COHEN ET AL

FILING DATE

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GROUP

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLAS	FILING DATE IF APPROPRIATE
AA	6329701	12/01	NGO ET AL	257	622	
AB	6245683	6/01	LIU, C. KAI	438	702	
AC	6153935	11/00	FORLSTEIN ET AL	257	276	
AD	6294835	9/01	DALAL ET AL	257	760	
AE	6245665	6/01	YOKOYAMA, TAKASHI	438	623	
AF	6207556	3/01	HSU	438	637	
AG						
AH						
AI						
AJ						
AK						

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLAS	TRANSLATION YES	TRANSLATION NO
AL							
AN							
AO							
AP							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

AR	"Pursuing the Perfect Low Dielectric," LAURA PETERS, SEMICONDUCTOR INTERNATIONAL MAGAZINE, SEPT./1998, 7 PAGES
AS	
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XAMINER

DATE CONSIDERE